

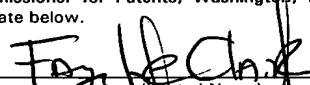
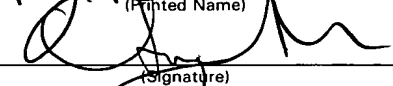
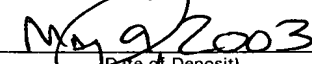
#6



Atty. Dkt. No. 039153-0433 (C167596-CIP)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sander, et al.
Title: MINIMIZING TRANSISTOR SIZE
IN INTEGRATED CIRCUITS
Appl. No.: 10/042,732
Filing Date: 04/25/2001
Examiner: Nguyen, T.
Art Unit: 2815

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date below.	
	(Printed Name)
	(Signature)
	(Date of Deposit)

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, D.C. 20231

Attn: Appl. Processing Division, Customer Correction Branch

Sir:

Attached is a copy of the official Filing Receipt received from the PTO in the above-referenced application.

There is an error with respect to the attorney docket number. The docket number should read: 039153-0433 (C167596-CIP).

The correction is not due to any error by Applicant and, accordingly, no fee is due. Issuance of a corrected Filing Receipt is respectfully requested.

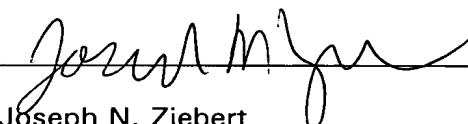
Respectfully submitted,

Date

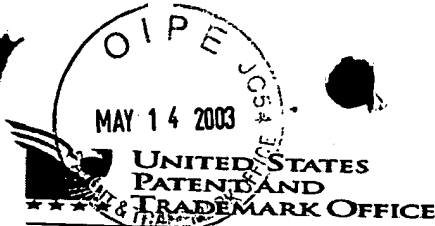
5-7-03

FOLEY & LARDNER
Suite 3800
777 East Wisconsin Avenue
Milwaukee, Wisconsin 53202-5306
Telephone: (414) 297-5768
Facsimile: (414) 297-4900

By



Joseph N. Ziebert
Attorney for Applicant
Registration No. 35,421



Commissioner for Patents
Washington, DC 20231
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
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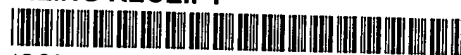
10/042,732	04/25/2001	2815	740	39153/433 (C167596)	5	20	3
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Joseph N. Ziebert
FOLEY & LARDNER
Firststar Center
777 East Wisconsin Avenue
Milwaukee, WI 53202-5367

39153-433
(C167596)

CONFIRMATION NO. 1672

FILING RECEIPT



OC00000009722250

Date Mailed: 03/27/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Sander, Residence Not Provided;

Assignment For Published Patent Application

Advanced Micro Devices, Inc.;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/515,875 02/29/2000 PAT 6,287,953
which is a CIP of 09/119,934 07/21/1998 PAT 6,146,954

Foreign Applications

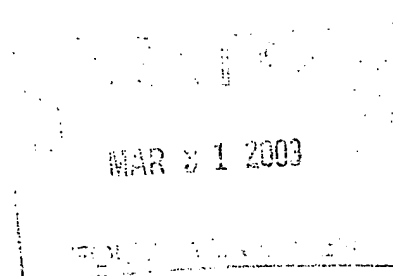
If Required, Foreign Filing License Granted: 03/27/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title



7 3/31/03

Minimizing transistor size in integrated circuits

Preliminary Class

257

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

GRANTED

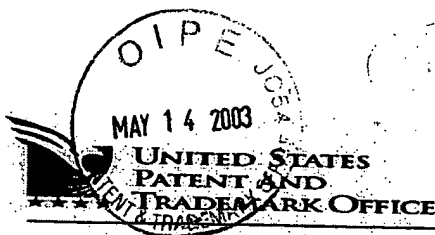
The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Office of Export Administration, Department of Commerce (15 CFR 370.10 (j)); the Office of Foreign Assets Control, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).



#5

Commissioner for Patents
Washington, DC 20231
www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/042,732	04/25/2001	Sander	39153/149 (C167596)

Joseph N. Ziebert
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Firstar Center
777 East Wisconsin Avenue
Milwaukee, WI 53202-5367

CONFIRMATION NO. 1672

FORMALITIES LETTER



OC000000009722251

Date Mailed: 03/27/2003

NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

05/15/2003 SZEWDIE1 00000041 10042732

01 FC:1051

130.00 OP

FILED UNDER 37 CFR 1.53(b)

*Filing Date Granted***Items Required To Avoid Abandonment:**

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is missing.
A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date, is required.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(e) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.

SUMMARY OF FEES DUE:

Total additional fee(s) required for this application is **\$130** for a Large Entity

- **\$130** Late oath or declaration Surcharge.

*A copy of this notice **MUST** be returned with the reply.*

Customer Service Center
Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE



Atty. Dkt. No. 039153-0433 (C167596-CIP)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sander, et al.

Title: MINIMIZING TRANSISTOR SIZE
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Appl. No.: 10/042,732

Filing Date: 04/25/2001

Examiner: Nguyen, T.

Art Unit: 2815

CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date below. Fayd Kachar (Printed Name) (Signature) May 9 2003 (Date of Deposit)
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**TRANSMITTAL OF MISSING PARTS
OF PATENT APPLICATION**

Commissioner for Patents
Washington, D.C. 20231

Attn: BOX MISSING PARTS

Sir:

In response to the Notice to File Missing Parts of Application mailed on March 27, 2003, in the above-identified application, transmitted herewith are the missing parts needed to complete the filing of the subject patent application.

Enclosed are:

[X] Copy of executed Declaration and Power of Attorney from parent file USSN 09/515,875 (Atty. Dkt. No. 39153-149) (5 pages).

[X] Return Copy of Notice to File Missing Parts (1 pg.)

[X] Check No. 12239 for \$130.00 for Late Oath or Declaration Surcharge.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 06-1447. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 06-1447.

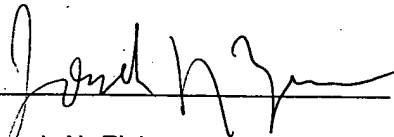
Atty. Dkt. No. 039153-0433 (C167596-CIP)

Respectfully submitted,

Date

5-9-03

By



Joseph N. Ziebert
Attorney for Applicant
Registration No. 35,421

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